

DEC 22 2005

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To:	Christos J. Georgiou, et al.	From:	Steven Fischman
Fax:	(571) 273-8300	Pages:	6 pages including cover sheet
Phone:		Date:	12/22/2005
Re:	U.S. Patent Appln. No. 10/604,491 Group Art Unit: 2111 Confirmation No: 1490 Docket No: FIS920030113US1 (16497)	CC:	

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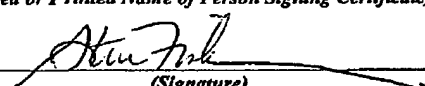
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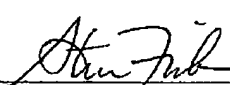
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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8) Applicant(s): Christos J. Georgiou, et al.			Docket No. FIS920030113US1 (16497)
Application No. 10/604,491	Filing Date July 25, 2003	Examiner Kenneth S. Kim	Group Art Unit 2111
Invention: SELF-CONTAINED PROCESSOR SUBSYSTEM AS COMPONENT FOR SYSTEM-ON-CHIP DESIGN			
<p>I hereby certify that this <u> RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143 </u> <i>(Identify type of correspondence)</i></p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u> (571) 273-8300 </u>)</p> <p>on <u> December 22, 2005 </u> <i>(Date)</i></p> <p style="text-align: right;"><u> Steven Fischman </u> <i>(Typed or Printed Name of Person Signing Certificate)</i></p> <p style="text-align: right;"><u>  </u> <i>(Signature)</i></p> <p style="text-align: center;">Note: Each paper must have its own certificate of mailing.</p>			


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TRANSMITTAL LETTER (General - Patent Pending)				Docket No. FIS920030113US1 (16497)	
In Re Application Of: Christos J. Georgiou, et al.					
Application No. 10/604,491	Filing Date July 25, 2003	Examiner Kenneth S. Kim	Customer No. 23389	Group Art Unit 2111	Confirmation No. 1490
Title: SELF-CONTAINED PROCESSOR SUBSYSTEM AS COMPONENT FOR SYSTEM-ON-CHIP DESIGN					
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 Signature			Dated: December 22, 2005		
Steven Fischman Registration No. 34,594					
SCULLY, SCOTT, MURPHY & PRESSER 400 Garden City Plaza, Suite 300 Garden City, New York 11530 (516) 742-4343					
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P16A/REV03

DEC 22 2005

TRANSMITTAL LETTER (General - Patent Pending)				Docket No. FIS920030113US1 (16497)	
In Re Application Of: Christos J. Georgiou, et al.					
Application No. 10/604,491	Filing Date July 25, 2003	Examiner Kenneth S. Kim	Customer No. 23389	Group Art Unit 2111	Confirmation No. 1490
Title: SELF-CONTAINED PROCESSOR SUBSYSTEM AS COMPONENT FOR SYSTEM-ON-CHIP DESIGN					
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